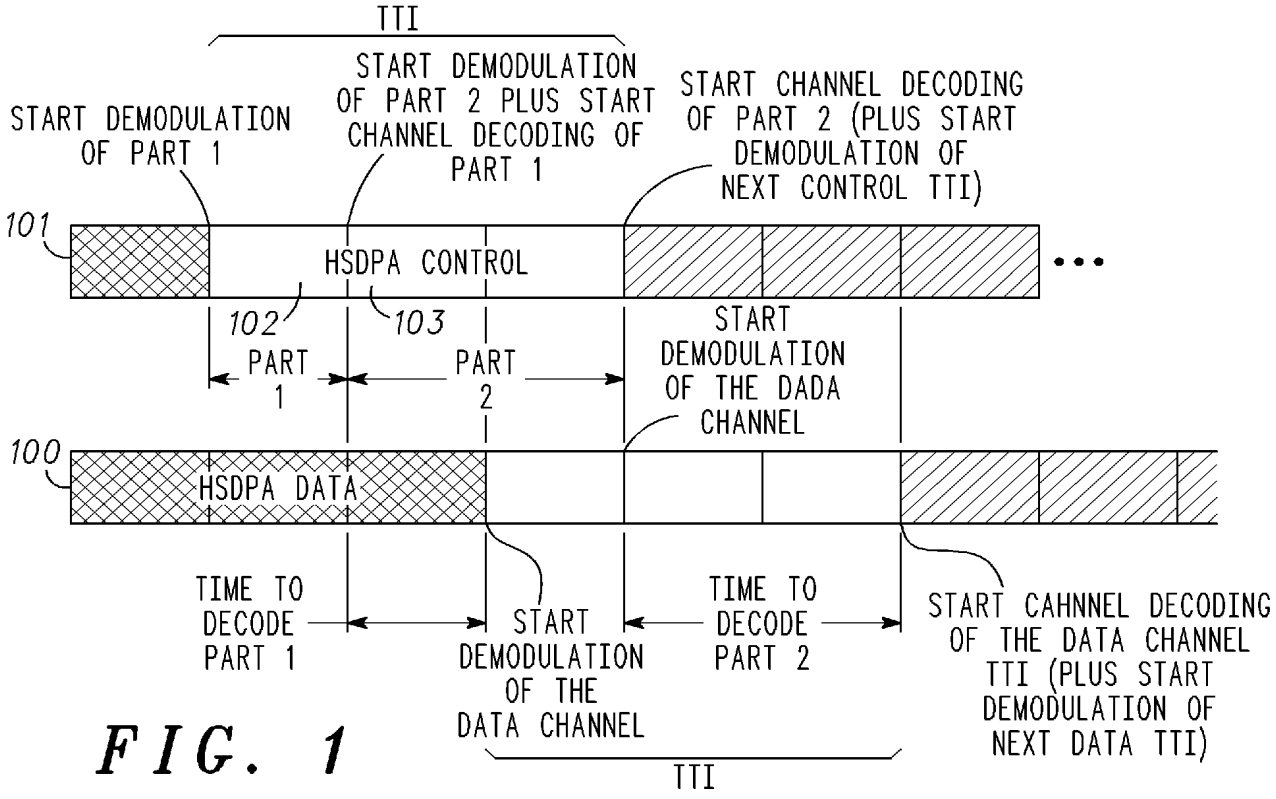
TTI
START DEMODULATION OF PART 1
START DEMODULATION OF PART 2 PLUS START CHANNEL DECODING OF PART 1
START CHANNEL DECODING OF PART 2 (PLUS START DEMODULATION OF NEXT CONTROL TTI)
101
HSDPA CONTROL
102
103
PART 1
PART 2
START DEMODULATION OF THE DADA CHANNEL
100
HSDPA DATA
TIME TO DECODE PART 1
START DEMODULATION OF THE DATA CHANNEL
TIME TO DECODE PART 2
START CAHNNEL DECODING OF THE DATA CHANNEL TTI (PLUS START DEMODULATION OF NEXT DATA TTI)
TTI

FIG. 1

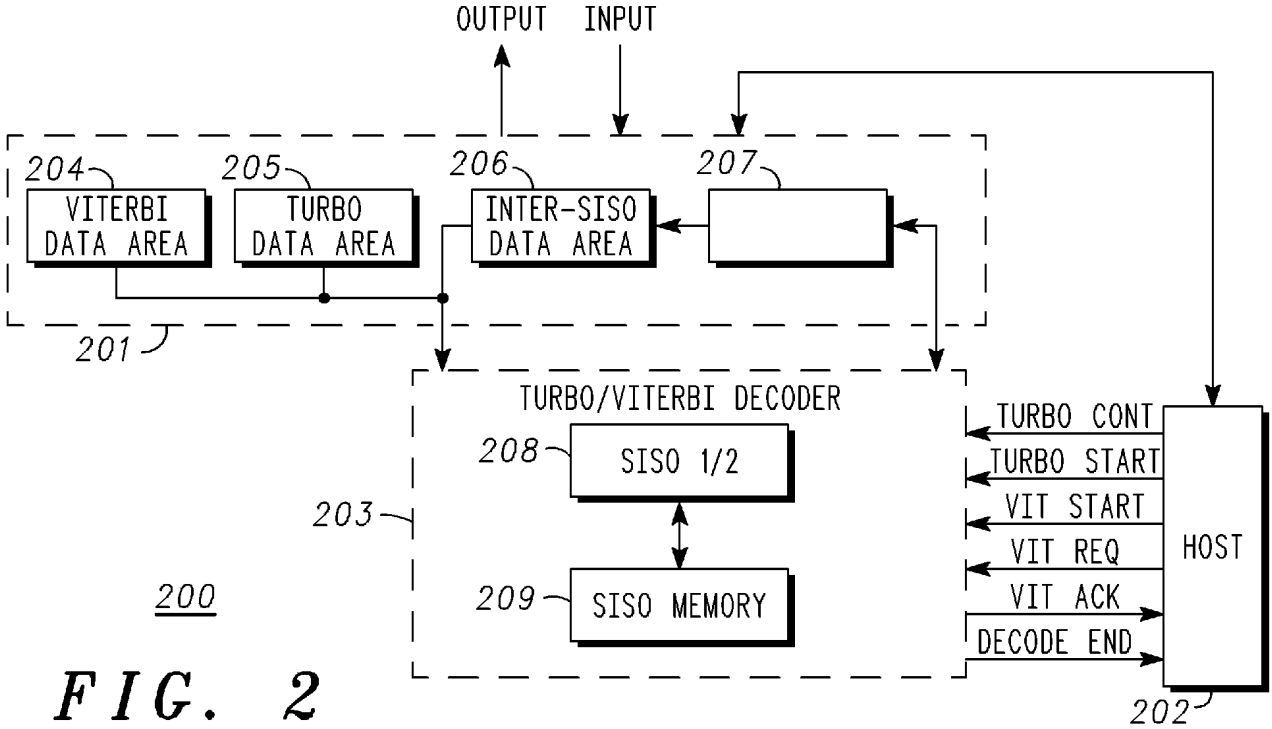
OUTPUT
INPUT
204
VITERBI DATA AREA
205
TURBO DATA AREA
206
INTER-SISO DATA AREA
207
201
TURBO/VITERBI DECODER
208
SISO 1/2
203
209
SISO MEMORY
200
TURBO CONT
TURBO START
VIT START
VIT REQ
VIT ACK
DECODE END
HOST
202

FIG. 2

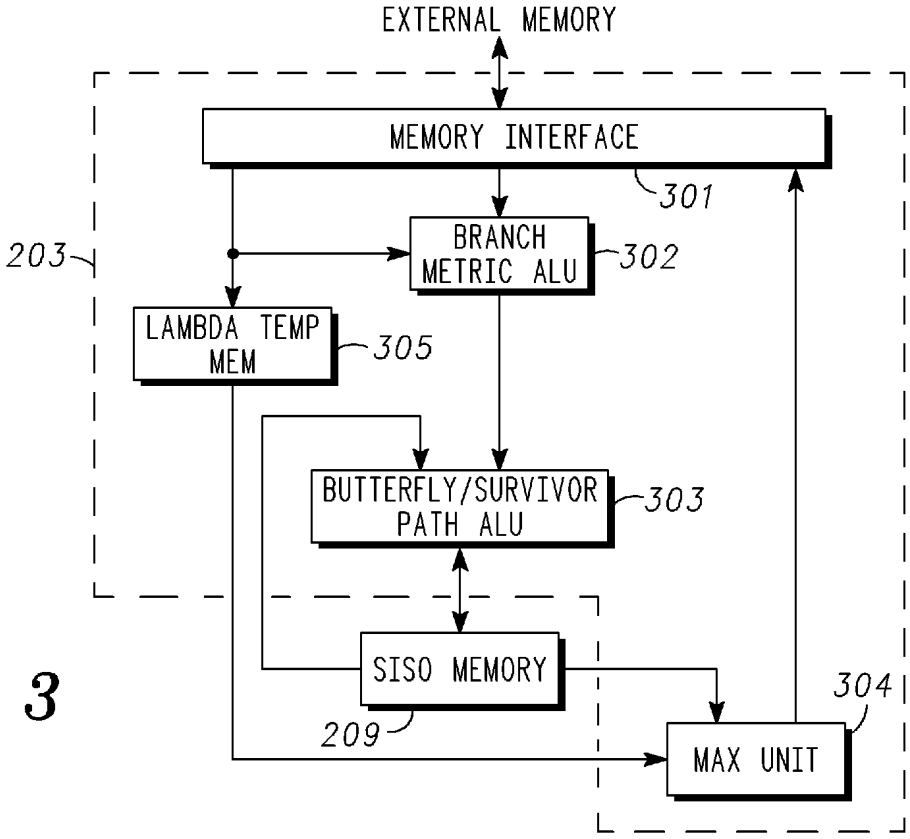


FIG. 3

DATA STORAGE FOR VITERBI DECODING

| | 16 BYTES | 16 BYTES |
|---|---|---|
| PART A | OLD P.M. AND S.P. 0-3 | OLD P.M. AND S.P. 4-7 |
| | ⋮ | ⋮ |
| | OLD P.M. AND S.P. 120-123<br>OLD P.M. AND S.P. 132-135 | OLD P.M. AND S.P. 124-127<br>OLD P.M. AND S.P. 128-131 |
| | ⋮ | ⋮ |
| | OLD P.M. AND S.P. 252-255 | OLD P.M. AND S.P. 248-251 |
| PART B | NEW P.M. AND S.P. 0-3 | NEW P.M. AND S.P. 4-7 |
| | ⋮ | ⋮ |
| | NEW P.M. AND S.P. 120-123<br>NEW P.M. AND S.P. 132-135 | NEW P.M. AND S.P. 124-127<br>NEW P.M. AND S.P. 128-131 |
| | ⋮ | ⋮ |
| | NEW P.M. AND S.P. 252-255 | NEW P.M. AND S.P. 248-251 |
| | MEMORY GROUP A | MEMORY GROUP B |

FIG. 4

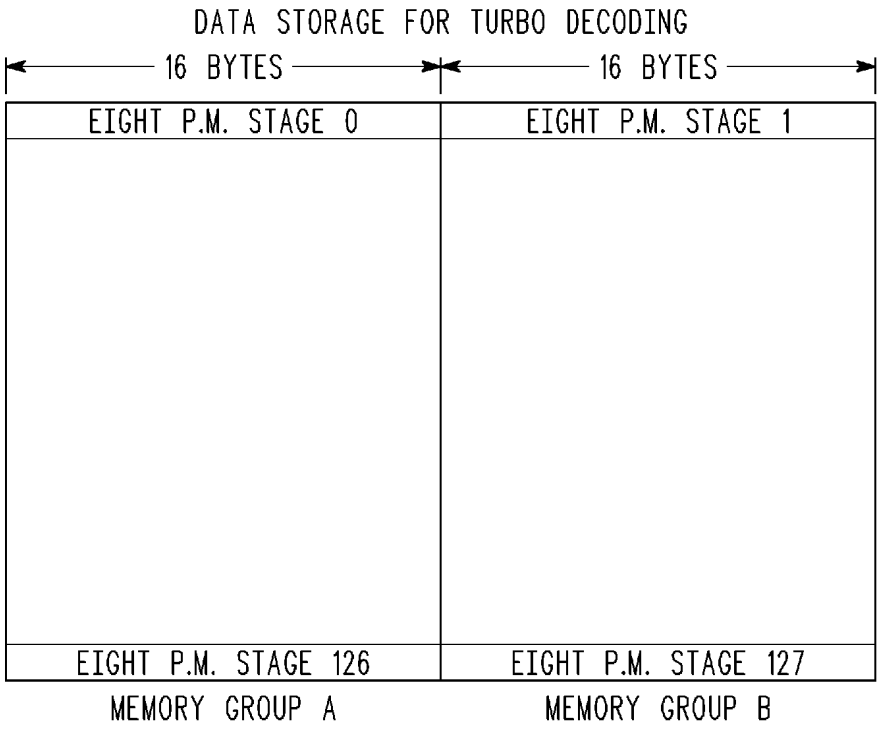
DATA STORAGE FOR TURBO DECODING
16 BYTES
16 BYTES
EIGHT P.M. STAGE 0
EIGHT P.M. STAGE 1
EIGHT P.M. STAGE 126
EIGHT P.M. STAGE 127
MEMORY GROUP A
MEMORY GROUP B

*FIG. 5*